Automatic Parallelization - Part 1

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NPTEL Course on Compiler Design

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- Automatic conversion of sequential programs to parallel programs by a compiler
- Target may be a vector processor (vectorization), a multi-core processor (concurrentization), or a cluster of loosely coupled distributed memory processors (parallelization)
- Parallelism extraction process is normally a source-to-source transformation
- Requires dependence analysis to determine the dependence between statements
- Implementation of available parallelism is also a challenge
 - For example, can all the iterations of a 2-nested loop be run in parallel?

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```
for I = 1 to 100 do {
    X(I) = X(I) + Y(I)
}
can be converted to
```

```
X(1:100) = X(1:100) + Y(1:100)
```

The above code can be run on a vector processor in O(1) time. The vectors X and Y are fetched first and then the vector X is written into

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```
for I = 1 to 100 do {
    X(I) = X(I) + Y(I)
}
```

```
can be converted to
```

```
forall I = 1 to 100 do {
    X(I) = X(I) + Y(I)
```

The above code can be run on a multi-core processor with all the 100 iterations running as separate threads. Each thread "owns" a different I value

```
for I = 1 to 100 do {
    X(I+1) = X(I) + Y(I)
}
```

cannot be converted to

```
X(2:101) = X(1:100) + Y(1:100)
```

because of dependence as shown below

```
X(2) = X(1) + Y(1)

X(3) = X(2) + Y(2)

X(4) = X(3) + Y(3)
```

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- Array subscripts should be linear functions of loop variables
- Loop lower bound should be one and the loop increment should be one
- A few loop transformations are carried out to ensure the above
 - Loop normalization
 - Induction variable substitution
 - Expression folding and forward substitution

Loop Normalization

Loop lower bound \rightarrow 1, and loop increment \rightarrow 1

Original Loop	Normalized Loop	
for I = 1 to 100 do {	for I = 1 to 100 do {	
KI = I	KI = I	
for J = 1 to 300 by 3 do	for J = 1 to 100 do	
{	{	
KI = KI + 2	KI = KI + 2	
U(J) = U(J)*W(KI)	U(3*J-2) = U(3*J-2)*W(KI)	
V(J+4) = V(J)+W(KI)	V(3*J+1) = V(3*J-2)+W(KI)	
}	}	
}	J = 301	
	}	

```
for I = 1 to 100 do {
  KI = I
  for J = 1 to 100 do {
    U(3*J-2) = U(3*J-2)*W(KI+2*J)
    V(3*J+1) = V(3*J-2)*W(KI+2*J)
  }
  KI = KI+200
  J = 301
}
```

Now KI is a *constant* in the J-loop. This is the inverse of *operator strength reduction*

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```
for I = 1 to 100 do {
  for J = 1 to 100 do {
    S1: U(3*J-2) = U(3*J-2)*W(I+2*J)
    S2: V(3*J+1) = V(3*J-2)*W(I+2*J)
  }
  KI = I+200 // may be deleted if KI is not live
  J = 301 // may be deleted if J is not live
}
```

Now all subscripts are linear functions of loop variables as needed for the dependence analysis.

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Vector Code Generation

$$I = 1, J = 1, S1: U(1) = U(1) + \dots$$

$$S2: V(4) = V(1) + \dots$$

$$J = 2, S1: U(2) = U(2) + \dots$$

$$S2: V(7) = V(4) + \dots$$

- The dependence S1 $\overline{\delta}_{(=,=)}$ S1 is harmless for vectorization of S1
- But, the dependence S2 $\delta_{(=,<)}$ S2 prevents vectorization of S2

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Data Dependence Relations



- Forward or "<" direction means dependence from iteration *i* to *i* + *k* (*i.e.*, computed in iteration *i* and used in iteration *i* + *k*)
- Backward or ">" direction means dependence from iteration *i* to *i* - *k* (*i.e.*, computed in iteration *i* and used in iteration *i* - *k*). This is not possible in single loops and possible in doubly or higher levels of nesting
- Equal or "=" direction means that dependence is in the same iteration (*i.e.*, computed in iteration *i* and used in iteration *i*)

Data Dependence Graph and Vectorization

- Individual nodes are statements of the program and edges depict data dependence among the statements
- If the DDG is acyclic, then vectorization of the program is straightforward
 - Vector code generation can be done using a topological sort order on the DDG
- Otherwise, find all the strongly connected components of the DDG, and reduce the DDG to an acyclic graph by treating each SCC as a single node
 - SCCs cannot be fully vectorized; the final code will contain some sequential loops and possibly some vector code

Data Dependence Graph and Vectorization

- Any dependence with a forward (<) direction in an outer loop will be satisfied by the serial execution of the outer loop
- If an outer loop L is run in sequential mode, then all the dependences with a forward (<) direction at the outer level (of L) will be automatically satisfied (even those of the loops inner to L)
- However, this is not true for those dependences with with

 (=) direction at the outer level; the dependences of the
 inner loops will have to be satisfied by appropriate
 statement ordering and loop execution order



X(1:99) = (/1:99/) B(1:99) = (/99:1:-1/) X(2:100) = G(B(1:99)) A(1:99) = F(X(1:99))

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for I = 1 to 100 do { X(I, 2:101, 1:100) = A(I, 1:100, 1:100) + 10 A(I+1, 2:101, 1:50) = X(I, 1:100, 1:50) + 5

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	l = 1	1 = 2
J=1 -	X(1,2,K) = A(1,1,K)	X(2,2,K) = A(2,1,K)
δ _{=,<} Ι	A(2,2,L) = X(1,1,L)	A(3,2,L) = X(2,1,L)
J=2	X(1,3,K) = A(1,2,K)	X(2,2,K) = A(2,2,K)
	A(2,3,L) = X(1,2,L)	A(3,3,L) = X(2,2,L)
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J=3	X(1,4,K) = A(1,3,K)	X(2,4,K) = A(2,3,K)
	A(2,4,L) = X(1,3,L)	A(3,4,L) = X(2,3,L)
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for I = 1 to 100 do { code for S2, S3, S4

S1: X(1:100) = Y(1:100) + 10

generated at higher levels



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Level 3 DDG for the composite node S2S3

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