## Automatic Parallelization - Part 4

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#### NPTEL Course on Compiler Design

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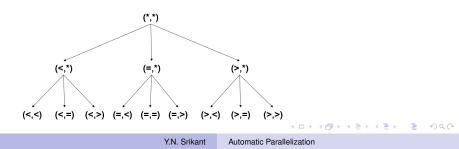
## Data Dependence Framework

- Given two array references (with *s* dimensions and nested in loop nest of depth *d*):
  - $S_{v}: X(f_{1}(I_{1},...,I_{d}), f_{2}(\bar{I}),...,f_{s}(\bar{I}))$
  - $S_{w}: X(g_{1}(I_{1},...,I_{d}), g_{2}(\bar{I}),...,g_{s}(\bar{I}))$ 
    - We test for both  $S_v \, \delta^* S_w$  and  $S_w \, \delta^* S_v$  simultaneously
    - The particular type of dependence  $(\delta, \overline{\delta}, or \delta^o)$  depends on the position of references (lhs or rhs) and the direction of dependence
- We first test to see if the array regions accessed by the two references intersect
  - Intersection will occur when the subscript functions are equal simultaneously

$$\begin{array}{rcl} f_1(i_1,...,i_d) &=& g_1(j_1,...,j_d) \\ f_2(i_1,...,i_d) &=& g_2(j_1,...,j_d) \\ & & \\ & & \\ f_s(i_1,...,i_d) &=& g_s(j_1,...,j_d) \end{array}$$

## Data Dependence Framework

- Test for intersection with a DV (\*, \*, ..., \*)
- If *independence* can be proven with this DV, then the regions accessed by the two references are disjoint
- Otherwise, one "\*" in the DV is refined to "<", "=", and ">", and testing is continued with these three refined DV
- Thus, testing is done by hierarchical expansion of one "\*" at a time
- If independence can be proven at any point in the hierarchy, then the DV beneath it need not be tested



#### Complement and Product of Direction Vectors

• Complement of a DV  $\Psi = (\Psi_1, ..., \Psi_d)$  is another DV  $\Psi^{-1} = (\Psi_1^{-1}, ..., \Psi_d^{-1})$ , where each  $\Psi_k^{-1}$  is computed from  $\Psi_k$  as follows

$$\begin{array}{|c|c|c|} |\Psi_k & <=> \leq \geq \neq * \\ \hline \Psi_k^{-1} & >= < \geq \leq \neq * \\ \end{array}$$

- Product of two DVs  $\Psi^1 = (\Psi_1^1, ..., \Psi_d^1)$  and  $\Psi^2 = (\Psi_1^2, ..., \Psi_d^2)$  is defined to be  $\Psi = (\Psi_1, ..., \Psi_d) = \Psi^1 \times \Psi^2$ , where  $\Psi_1 = \Psi_1^1 \times \Psi_1^2$ ,  $\Psi_2 = \Psi_2^1 \times \Psi_2^2$ ,...,  $\Psi_d = \Psi_d^1 \times \Psi_d^2$ , and  $\times$  is defined on DV elements as follows
- "." means a null DV element

#### Product of DV elements

X	<	=	>	≤	2	¥	*
<	<	•	•	<	•	<	<
=		=	•	=	=		=
>		•	>	•	>	>	>
≤	<	=	-	≤	=	<	≤
2	•	=	>	=	2	>	2
¥	<	•	>	<	>	¥	¥
*	<	=	>	≤	2	¥	*

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 Compute product of different DV corresponding to various subscripts to get one DV

 $\Psi = \Psi_1 \times \Psi_2 \times ... \times \Psi_s$ 

- If this combination produces any "." entries, then there is no simultaneous intersection at all and so there can be no dependence
- To get the data dependence DV, we must intersect  $\Psi$  with the execution order DV:  $\Psi_{\nu \to w} = \Psi \times \Omega_{\nu \to w}$
- If this produces any "." entries, there is no dependence from S<sub>v</sub> to S<sub>w</sub>

- If all the entries are valid, we add the data dependence relation: S<sub>ν</sub> δ<sup>\*</sup><sub>ν→w</sub> S<sub>w</sub> to the DDG
- The actual type of dependence  $(\delta, \overline{\delta}, or \delta^o)$  will depend on the position of the references
- To check dependence from  $S_w$  to  $S_v$ , we compute:  $\Psi_{w \to v} = \Psi^{-1} \times \Omega_{w \to v}$
- If all the entries are valid, we add the data dependence relation: S<sub>w</sub> δ<sup>\*</sup><sub>w→ν</sub> S<sub>ν</sub> to the DDG

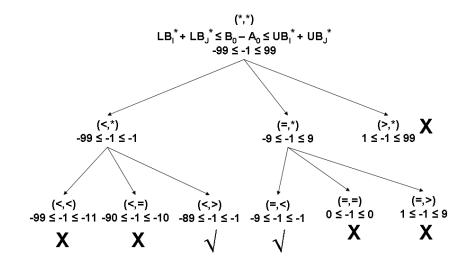
## Data Dependence Framework Test Example - 2.1

Given program:

```
for I = 1 to 10 do {
    for J = 1 to 10 do {
        S1: A(I*10+J) = ...
        S2: ... = A(I*10+J-1)
        }
    }
}
```

- Dependence equation:  $10I_1 + J_1 10I_2 J_2 = -1$
- GCD Test with (\*,\*): GCD(10, 1, -10, -1) divides -1, which is true and hence dependence exists. Now we need to apply Banerjee's test

#### Data Dependence Framework Test Example - 2.2



- The dependence test returns two DVs: (<,>) and (=,<)
- There is only one subscript
- Recall that S1  $\Theta_{(=,\leq)}$ S2, S2  $\Theta_{(=,<)}$ S1, S1  $\Theta_{(<,*)}$ S2, S2  $\Theta_{(<,*)}$ S1, are all possible
- Intersect these with the execution order DVs

 $(<,>) \times (<,*) = (<,>)$  $(=,<) \times (=,\leq) = (=,<)$ Other products produce "." values

- Therefore we get: S1  $\delta_{(=,<)}$ S2 and S1  $\delta_{(<,>)}$ S2
- There is no need to test S2 δ\* S1, since not all entries are "."

## Concurrentization or Parallelization

- If all the dependence relations in a loop nest have a direction vector value of "=" for a loop, then the iterations of that loop can be executed in parallel with no synchronization between iterations
- Any dependence with a forward (<) direction in an outer loop will be satisfied by the serial execution of the outer loop
- If an outer loop L is run in sequential mode, then all the dependences with a forward (<) direction at the outer level (of L) will be automatically satisfied (even those of the loops inner to L)
- However, this is not true for those dependences with (=) direction at the outer level; the dependences of the inner loops will have to be satisfied by appropriate statement ordering and loop execution order

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#### **Concurrentization Examples**

for I = 2 to N do { for J = 2 to N do { S1: A(I,J) = B(I,J) + 2 S2: B(I,J) = A(I-1, J-1) - B(I,J) } }

S1  $\delta_{(<,<)}$  S2, S1  $\overline{\delta}_{(=,=)}$  S2, S2  $\overline{\delta}_{(=,=)}$  S2

S1  $\overline{\delta}_{(=,<)}$  S2, S1  $\overline{\delta}_{(=,=)}$  S2, S2  $\overline{\delta}_{(=,=)}$  S2

	l = 1	= 2		l = 1	l = 2
J = 1	A(2,2)=	A(3,2)=	J = 1	A(2,2)=	A(3,2)=
	= A(1,1)	= A(2,1)		= A(2,1)	= A(3,1)
J = 2	A(2,3)=	A(3,3)=	J = 2	A(2,3)=	A(3,3)=
	= A(1,2)	= A(2,2)		= A(2,2)	= A(3,2)
J = 3	A(2,4)=	A(3,4)=	J = 3	A(2,4)=	A(3,4)=
	= A(1,3)	= A(2,3)		= A(2,3)	= A(3,3)

If the I loop is run in serial mode then, the J loop can be run in parallel mode The J loop cannot be run in parallel mode. However, the I loop can be run in parallel mode

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## Loop Transformations for increasing Parallelism

- Recurrence breaking
  - Ignorable cycles
  - Scalar expansion
  - Scalar renaming
  - Node splitting
  - Threshold detection and index set splitting
  - If-conversion
- Loop interchanging
- Loop fission
- Loop fusion

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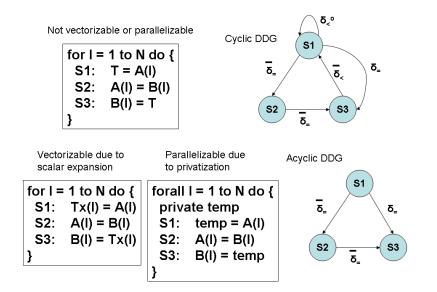
#### Ignorable Cycles

- Any single statement recurrence based on  $\overline{\delta}$  may be ignored
- The program:

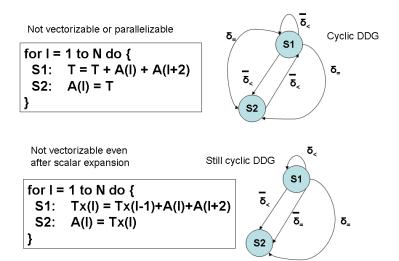
```
for I = 2 to 100 do {
S: X(I-1) = F(X(I))
}
```

has the dependence  $S \overline{\delta} S$ , but it can be vectorized as follows: X(1:99) = F(X(2:100))

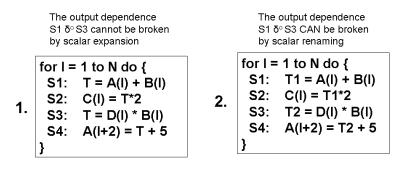
## Scalar Expansion



## Scalar Expansion is not always profitable



## Scalar Renaming

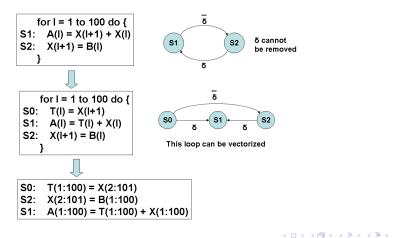


3. 
$$\begin{array}{ll} S3: & T2(1:100) = D(1:100) * B(1:100) \\ S4: & A(3:102) = T2(1:100) + 5(1:100) \\ S1: & T1(1:100) = A(1:100) + B(1:100) \\ S2: & C(1:100) = T1(1:100) * 2(1:100) \\ & T = T2(100) \end{array}$$

5(1:100) and 2(1:100) are vectors of constants

# Node Splitting

 Node splitting can be used in breaking a cycle consisting of an anti-dependence, but this introduces new temporary arrays



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#### Thresholds

for I = 1 to 100 do { X(I+5) = X(I) }

for I = 1 to 20 do {

for J = 1 to 5 do {

X(I\*5+J) = X(I\*5+J-5)

Cannot be vectorized Threshold value = 5

Thresholds can be found by modifications of Banjerjee's test

Cannot be vectorized Threshold value = 50

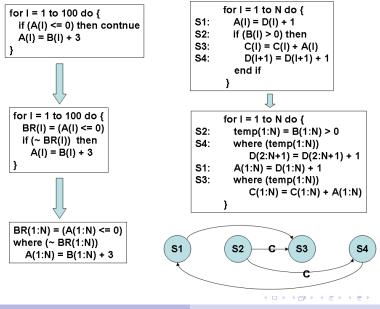
for I = 1 to 100 do { A(I) = A(101- I)

for I = 1 to 20 do { X(5\*I+1 : 5\*I+5) = X(5\*I-4 : 5\*I)

for J = 1 to 2 do { A(50\*J – 49 : 50\*J) = A(150 - 50\*I : 101-50\*I)

(I) (II) (II) (II) (II) (III)

#### **If-Conversion**



Y.N. Srikant Automatic Parallelization

- For machines with vector instructions, loops can be interchanged to find vector operations, if the original inner loop cannot be vectorized
- For multi-core and multi-processor machines, parallel outer loops are preferred and loop interchange may help to make this happen
- Requirements for simple loop interchange
  - The loops L1 and L2 must be tightly nested (no statements between loops)
  - 2 The loop limits of L2 must be invariant in L1
  - There are no statements  $S_v$  and  $S_w$  (not necessarily distinct) in L1 with a dependence  $S_v \delta^*_{(<,>)} S_w$

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#### Loop Interchange for Vectorizability

Inner loop is not vectorizable

for J = 1 to N do {  
for I = 1 to N do {  
S: 
$$A(I,J+1) = A(I,J) * B(I,J) + C(I,J)$$
  
}  
S  $\delta_{(<,=)} S$ 

#### Loop Interchange for parallelizability

A(I+1,J) = A(I,J) \* B(I,J) + C(I,J)

for J = 1 to N do {

S:

}

for I = 1 to N do {

Outer loop is not parallelizable, but inner loop is

 $\begin{array}{l} S \hspace{0.1cm} \delta_{(<,=)} \hspace{0.1cm} S \\ \text{Less work per thread} \end{array}$ 

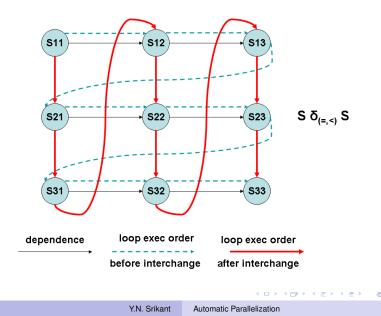
Outer loop is parallelizable but inner loop is not

S δ<sub>(=,<)</sub> S More work per thread

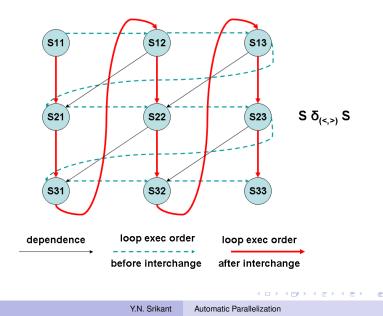
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forall J = 1 to N do { for I = 1 to N do { S: A(I+1,J) = A(I,J) \* B(I,J) + C(I,J) }

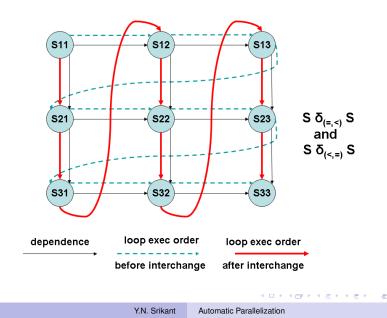
#### Legal Loop Interchange



#### Illegal Loop Interchange



#### Legal but not beneficial Loop Interchange

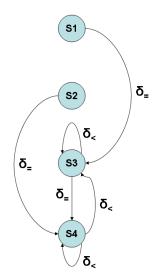


# $\begin{array}{ll} \mbox{for } I = 1 \mbox{ to } N \mbox{ do } \{ \\ \mbox{S1:} & A(I) = E(I) + 1 \\ \mbox{S2:} & B(I) = F(I) * 2 \\ \mbox{S3:} & C(I+1) = C(I) * A(I) + D(I) \\ \mbox{S4:} & D(I+1) = C(I+1) * B(I) + D(I) \\ \mbox{} \} \end{array}$

The above loop cannot be vectorized

L1: for I = 1 to N do { S1: A(I) = E(I) + 1 S2: B(I) = F(I) \* 2 } L2: for I = 1 to N do { S3: C(I+1) = C(I) \* A(I) + D(I) S4: D(I+1) = C(I+1) \* B(I) + D(I) }

L1 can be vectorized, but L2 cannot be

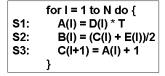


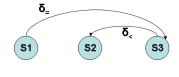
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- Lemma: If a loop L contains statements S<sub>k</sub> and S<sub>j</sub>, where S<sub>K</sub> follows S<sub>j</sub> in the loop and S<sub>k</sub> δ<sup>\*</sup><sub><</sub> S<sub>j</sub>, then loop fission may not split the loop at any point between S<sub>j</sub> and S<sub>k</sub>
- Loop fission may not be used to break a cycle of dependence into separate loops

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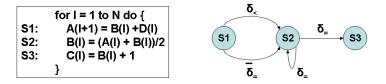
#### Loop Fission: Legal and Illegal





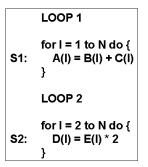
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In the above loop, S3  $\delta_{<}$  S2, and S3 follows S2. Therefore, cutting the loop between S2 and S3 is illegal. However, cutting the loop between S1 and S2 is legal.



The above loop can be cut between S1 and S2, and also between S2 and S3

- Same index sets
- Loops must be adjacent
- No conditional branch (that exits) in either loop (unless the conditions are identical)
- I/O in *both* loops makes fusion illegal, but I/O in one of the loops is permitted
- Data dependence requirement (later)



The above loops are not fusible

```
Option for LOOP 1

A(1) = B(1) + C(1)

for I = 2 to N do {

S1: A(I) = B(I) + C(I)

}
```

```
Options for LOOP 2

Option A

for I = 1 to N do {

S2: D(I+1) = E(I+1) * 2

}

Option B

for I = 1 to N do {

S2: if (I >= 2)

D(I) = E(I) * 2

}
```

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#### Illegal loop fusion

If the two loops are fused, then the dependences change!





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## Augmented Direction Vector

- Let S1 be a statement enclosed in a loop L1 with index set i, and let S2 be a statement enclosed in a loop L2 with index set j, and let the two index sets be identical. Let X be one of {δ, δ, δ<sup>o</sup>} and let S1 X S2.
- We define the *augmented DV* to be (?) where,
  - ?  $\in \{<,=,>\}$  and we say S1 X(?) S2 when
    - there exist particular iterations of S1 and S2, say, S1(i') and S2(j') with S1(i') X S2(j') and
       i' ? i'
- Definition 1 above allows a DV to have positions for loops that do not contain both S1 and S2
- Lemma: Let L1 and L2 be loops as above. If there are any statements  $S_j$  in L1 and  $S_k$  in L2 with  $S_j \delta^*(>) S_k$ , then fusing the loops is illegal

#### Augmented DV example

for I = 2 to N do { S1: A(I) = D(I) \* 2 } for I = 2 to N do { S2: B(I) = A(I) + 1 }

S1 δ(=) S2

S1 δ(<) S2

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S1 ō(>) S2