Module 2 (Lectures 3-10) Computer organization

1. Which of the following is NOT a CPU special purpose register?
   a) program counter
   b) instruction register
   c) processor status register
   d) general purpose register
2. Which of the following addressing modes uses an addition operation in order to determine the address of a memory operand?
   a) immediate
   b) absolute
   c) base displacement
   d) register indirect
3. In implementing a function call/return mechanism using MIPS 1 instructions, we did NOT use the stack for saving
   a) return address
   b) pointer to the current top of stack
   c) values of function local variables
   d) general purpose register values
4. Sign extension can be used to expand an 8 bit value into a 32 bit value by filling the most significant 24 bits of the expanded value with
   a) 0's
   b) 1's
   c) the most significant bit of the 8 bit value
   d) the most frequently occurring bit of the 8 bit value
5. As part of instruction execution on a processor implementing the MIPS 1 instruction set architecture, if the instruction is not a control transfer instruction, then in order to go on to the next instruction, the PC must be incremented by
   a) 1
   b) 2
   c) 4
   d) 8